

NOV - 7 2005

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: Attorney Docket No.: 3339.02US01  
Furo et al. Confirmation No.: 3943  
Application No.: 10/087,566 Examiner: Ling X. Xu  
Filed: March 1, 2002 Group Art Unit: 1775  
For: INSULATING SUBSTRATE BOARDS FOR SEMICONDUCTOR AND POWER  
MODULES

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INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

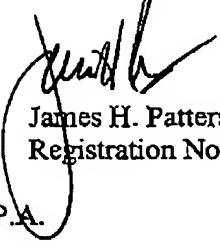
Pursuant to 37 C.F.R. § 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. The listing of a reference herein is not an admission that the reference is prior art or that the reference is material to patentability.

This Information Disclosure Statement is being filed with the RCE application.  
No certification or fee is required.

Application No. 10/087,566

U.S. Patent No. 6,033,787 was cited by the Chinese Patent Office in corresponding Chinese Application No. 02106821.6. A translation of the Office Action is enclosed herewith.

Respectfully submitted,



James H. Patterson  
Registration No. 30,673

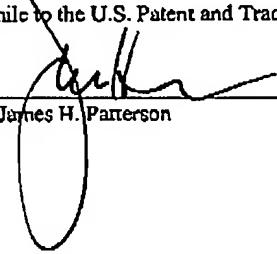
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*Please grant any extension of time necessary for entry; charge any fee due to Deposit Account No. 16-0631.*

## CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office, Fax No. 571-273-8300 on the date shown below.

November 7, 2005  
Date



James H. Patterson

